

<b>Notice of References Cited</b>	Application/Control No. 10/743,113		Applicant(s)/Patent Under Reexamination JUNG, KYUNG YUN	
	Examiner Johannes P. Mondt		Art Unit 3663	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,248,632	09-1993	Tung et al.	438/600
*	B	US-5,533,635	07-1996	Man, Chiu-Kwan	216/67
*	C	US-6,141,240	10-2000	Madan et al.	365/156
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	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf, "Silicon Processing for the VLSI Era", Volume 3 - "The submicron MOSFET", LATTICE PRESS, Sunset Beach, California (USA), 1995; in particular Chapter 3, "MOS Transistor Device Physics: Part 1, Basic Physics and the MOS Capacitor", especially pages 83-85.
	V	
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.